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Docket No.: M4065.0845/P845
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Eric R. Fossum, et al

Conf. #9540

Application No.: 09/211,718

Group Art Unit: 2615

Filed: December 14, 1998

Examiner: B. Genco

For: THREE SIDED BUTTABLE CMOS IMAGE
SENSOR

AMENDMENT

Box Non-Fee Amendment
Commissioner for Patents
Washington, DC 20231

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FEB 10 2003

Technology Center 2600

Dear Sir:

In response to the Office Action dated November 6, 2002 (Paper No. 2), please amend the above-identified U.S. patent application as follows:

In the Specification

Please replace the paragraph beginning on page 4, line 3, and ending on page 4, line 5 with the following new paragraph:

a1 There can be a small space 107 between the two adjacent chips 106, 109 due to the roughness of the edges. The small space is typically of the order of 1µm.

Please replace the paragraph beginning on page 5, line 4, and ending on page 5, line 10 with the following new paragraph:

a2 Hence, the pixels 204 are adjacent pixels 202 separated by a space that is preferably